



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Min-su Kim, et al.

Examiner: Hu, S.

Serial No.:

09/994,146

Group Art Unit: 2811

Filing Date:

November 26, 2001

Title:

SEMICONDUCTOR DEVICE HAVING SILICON-ON-INSULATOR

STRUCTURE AND METHOD OF FABRICATING THE SAME

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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Sir:

AMENDMENT

AMENDMENT

SEP - 5

2003

The following is in response to the Office Action dated June 11, 2003. Please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claim which begins on page 3 of this paper.

Remarks/Arguments begin on page 8 of this paper.